S-RIO Server Concept Prove Demo

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Reuse the existing Resource

- Concurrent AM 93x/x1x AMC Module
  - The 3rd generation Intel Core Processor and Mobile Intel QM77 Express Chipset
  - IDT PCIe to S-RIO bridge chip

- Concurrent Fabric Interconnect Networking Software (FIN-S)

- IDT Gen2 Switch Evaluation Board
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![Diagram of S-RIO Server Concept Proving Demo](image)

- **Client**
- **Server**

Diagram showing connections between components:
- I7 to TSI721 (PCIe x4)
- TSI721 to IDT S-RIO Gen2 Switch (PCIe x4)
- IDT S-RIO Gen2 Switch to TSI721 (PCIe x4)
- TSI721 to I7 (PCIe x4)
The demo is now running Concurrent’s FIN-S software with the standard Linux “Iperf” application as well.
At S-RIO Gen1 speeds (3.125G), the bandwidth is 9.2 Gbits/s (x4 port)

At S-RIO Gen2 speeds (5G), the bandwidth is 14.5 Gbits/s (x4 port)