

RapidIO® Developer Summit – Boston

May 10, 2005 – Babson Executive Conference Center

Schedule*

Updated May 04, 2005

Time	Yukon	Potomac	Susquehanna
08:00	Registration/Coffee/Pastries (1:00)		
09:00	Welcome Message (0:10) [Iain Scott, Executive Director, RTA] Why RapidIO Technology? (0:30) [Ernie Bergstrom, Principal Analyst, Crystal Cube Consulting LLC]		
09:40	Session A0 (0:50) RapidIO Technology Overview and Applications [RTA]		
10:30	Snack Break (0:15)		
10:45	Session A1 (1:00) Interconnect Comparison: Gigabit Ethernet vs. RapidIO [Freescale]	Session B1 (1:00) Designing a 480 Processor Multi-Computer with RapidIO Technology [Mercury]	Technology Lab [Open 10:30am–5:30pm]
11:45	Session A2 (0:40) Serial RapidIO Enables System Performance Gain for Infrastructure Video, Telecom and Imaging Applications [Texas Instruments]	Session B2 (0:40) RapidIO and Transparent Distributed Processing [QNX]	
12:25	Session A3 (0:30) Advantages of Packet Switched Communication in Next-Generation Wireless Equipment [IDT]	Session B3 (0:30) Addressing System-Wide Issues When Implementing Serial RapidIO at the Chip Level [Jennic]	
12:55	Lunch in Cafeteria (1:05)		
14:00	Session A4 (0:50) RapidIO and ATCA Design: Lessons Learned [Mercury/Tundra]	Session B4 (0:50) The Role of RapidIO Technology in Voice, Video, and Data Convergence [Erlang]	
14:50	Session A5 (0:30) RapidIO Software Initialization and Discovery [Tundra]	Session B5 (0:30) Development and Testing of FPGA-based RapidIO ATCA Line Cards [Xilinx]	
15:20	Snack Break (0:15)		
15:35	Session A6 (1:00) RapidIO & ATCA Panel [Moderated by Ernie Bergstrom]	Session B6 (1:00) Efficient, Reliable Messaging for RapidIO Technology [Enea]	
16:35	Closing Remarks (0:10) [Iain Scott, Executive Director, RTA]		
16:45			Prize Drawings at 5pm!

*NOTE: The schedule is subject to change.